In the Claims

This listing of claims will replace all prior versions and listings of claims in the application:

- 1 (Currently Amended) A data routing unit comprising:
- 2 a data receiver:

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- 3 a data transmitter:
- a bridge circuit connected to supply data to said data receiver and to receive data from said data transmitter, said 6 bridge circuit connected to at least one set of data input lines 7 and at least one set of data output lines, said bridge circuit responsive to a header of a data packet received from said data 8 9 transmitter or received from said at least one set of data input 10 lines to selectively route said received data packet to (1) said data receiver circuit, (2) a selected set of said at least one set 11 of data output lines, or (3) both said data receiver circuit and a 12 13 selected set of said at least one set of data output lines 14 dependent upon said header; and
 - an input/output memory connected to said data receiver for storing data received by said data receiver and to said data transmitter for storing data to be transmitted by said data transmitter, said input/output memory further storing transmitter scripts for controlling data transmission by said data transmitter; said data receiver generating an interrupt to said data transmitter to transmit predetermined response data upon detection of a predetermined receiver event signaled in said data packet; and said data transmitter executing a transmitter script stored at a predetermined location within said input/output memory upon interruption by said data receiver.

- 1 2. (Original) The data routing unit of claim 1, wherein:
- 2 said data transmitter transmits a transmitter receipt signal
- 3 back to a source of said data packet upon detection of said
- 4 predetermined receiver event.

(Canceled)

- 1 4. (Currently Amended) The data routing unit of claim $\frac{1}{2}$,
- 2 wherein:
- 3 said data receiver stores a portion of said received data
- 4 packet as a transmitter script at said predetermined location
- 5 within said input/output memory.
- 5. (Currently Amended) The data routing unit of claim $\frac{3}{2}$,
- 2 wherein:
- 3 said data receiver stores a portion of said received data
- 4 packet at said predetermined location within said input/output
- 5 memory as a pointer to a transmitter script; and
- 6 upon receipt of said interrupt said data transmitter executes
- 7 said transmitter script located within said input/output memory
- 8 corresponding to said pointer.